

FIG. 1

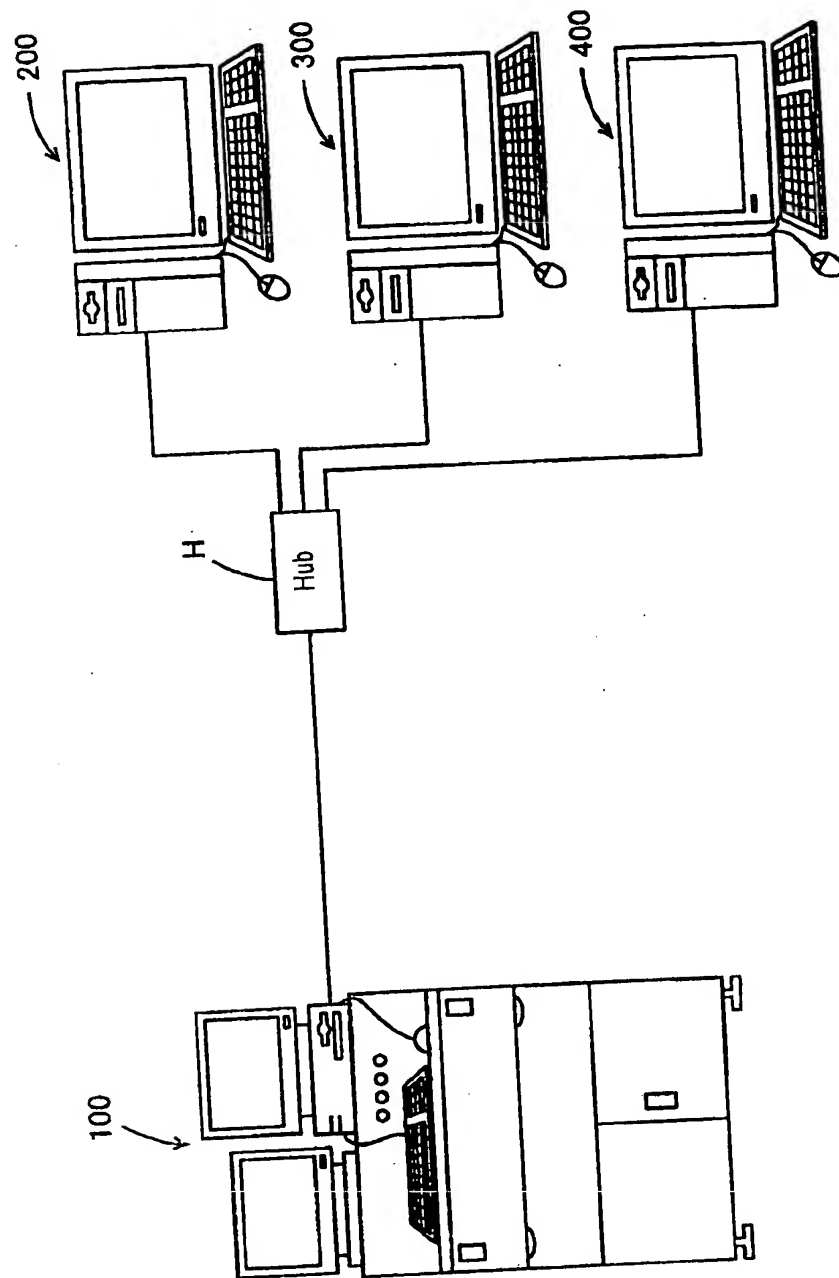


FIG. 2

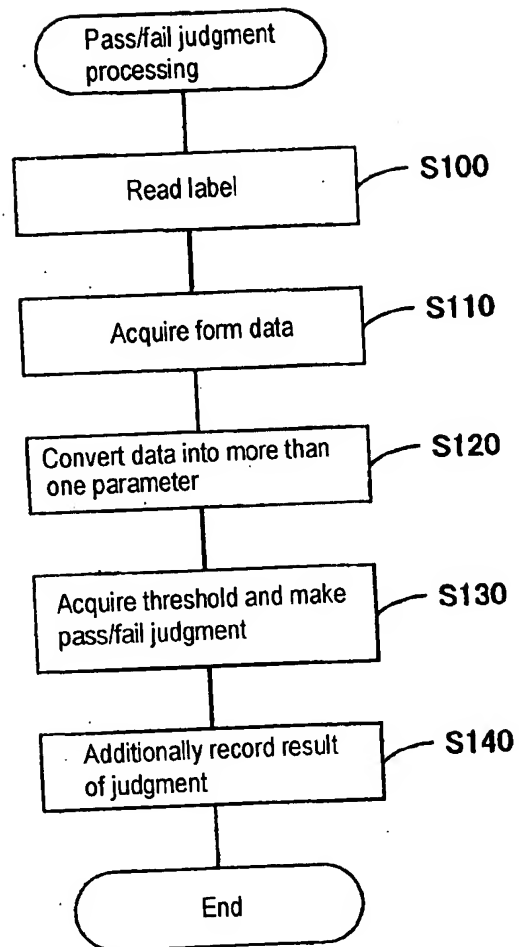


FIG 3

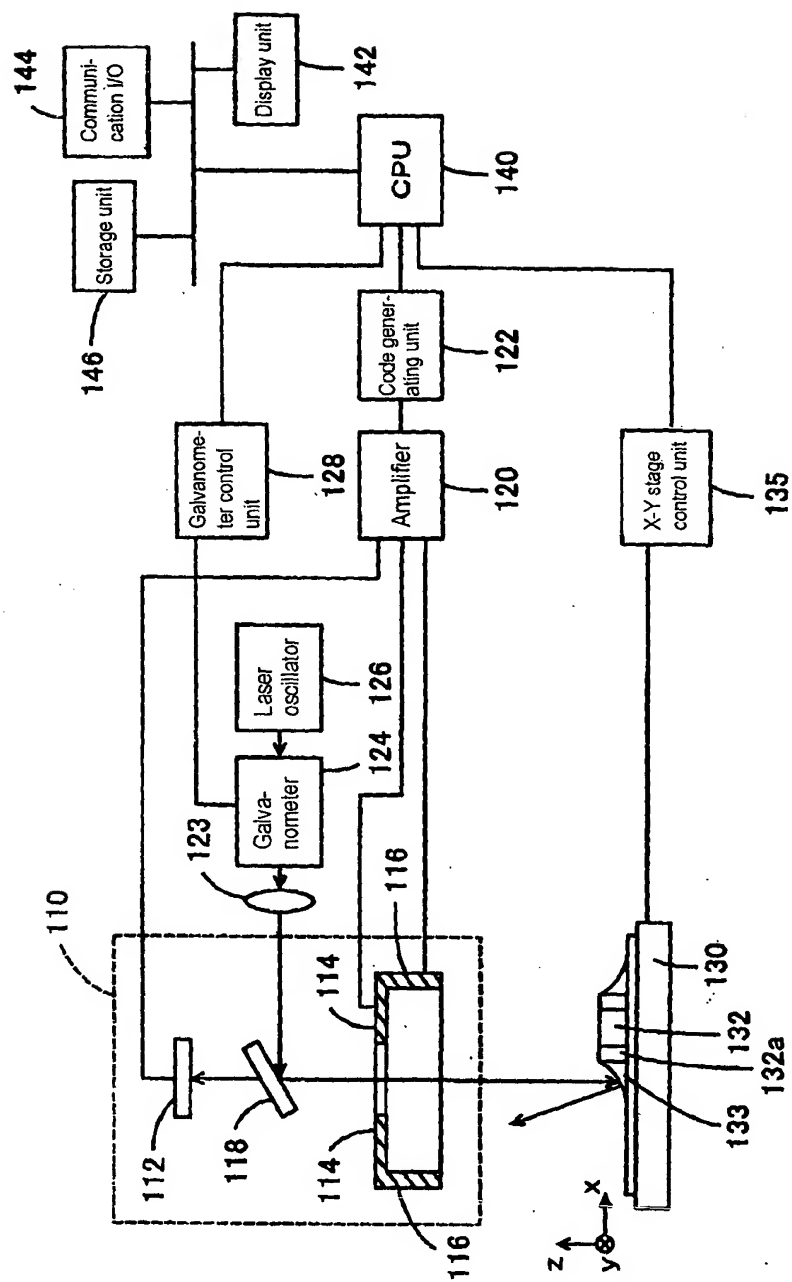


FIG. 4

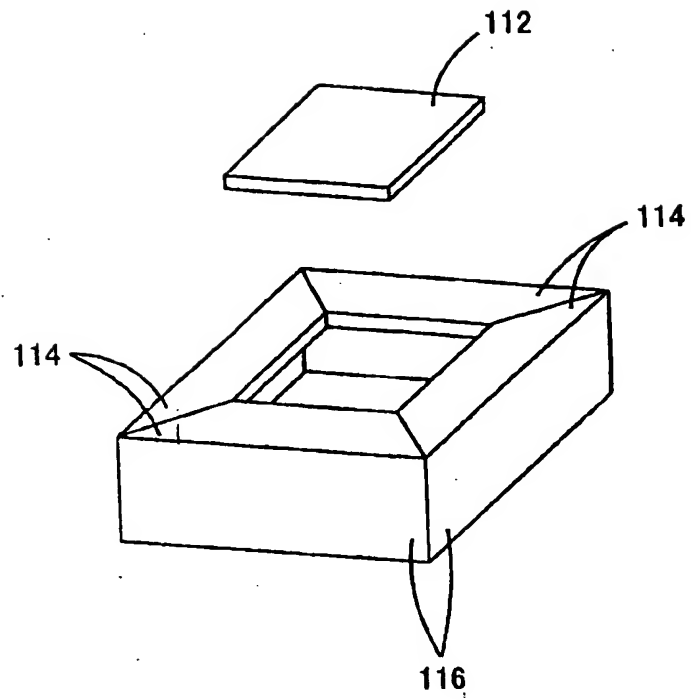


FIG. 5

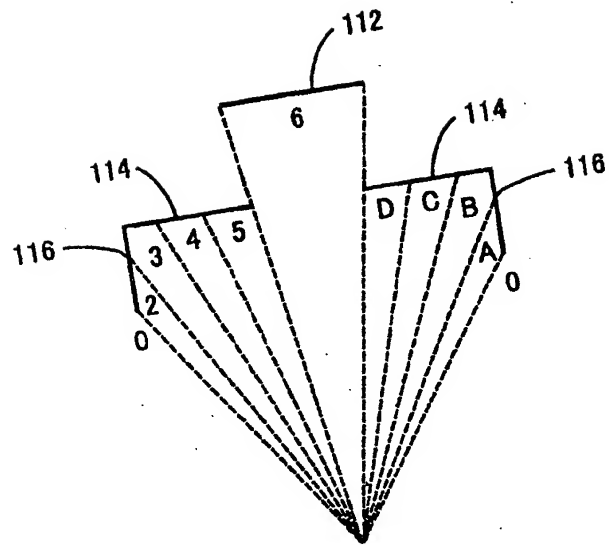


FIG. 6

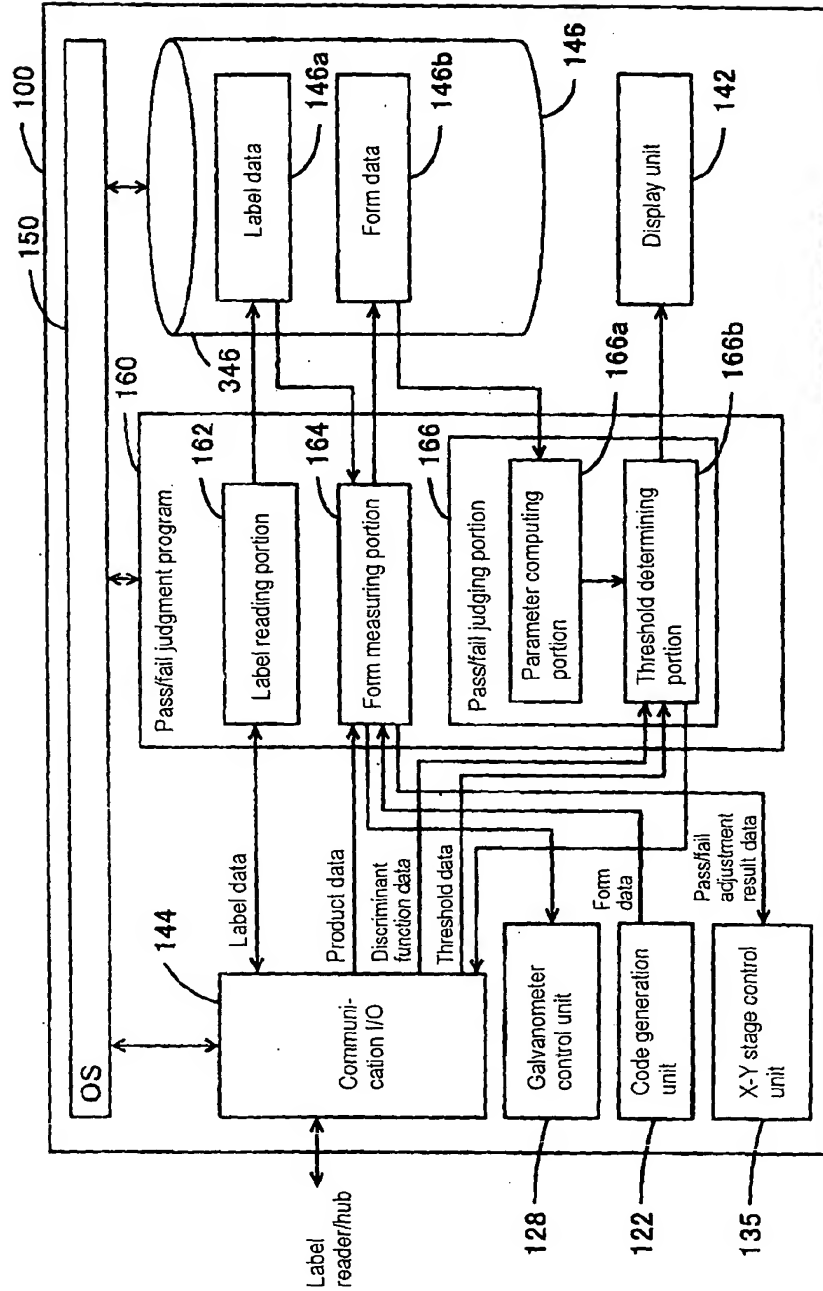


FIG. 7

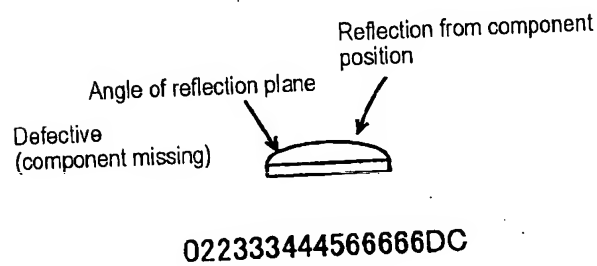
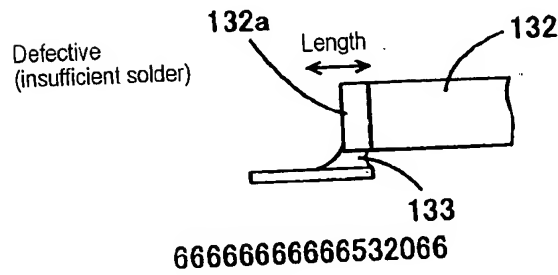
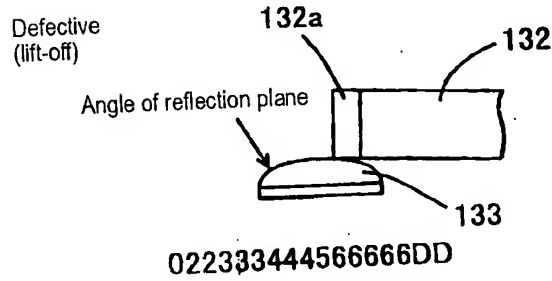
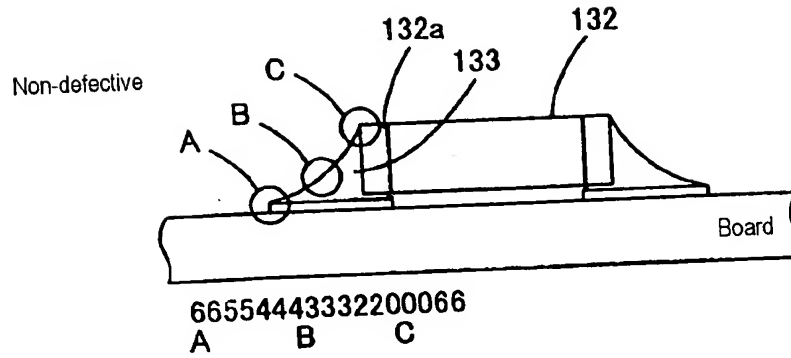


FIG. 8

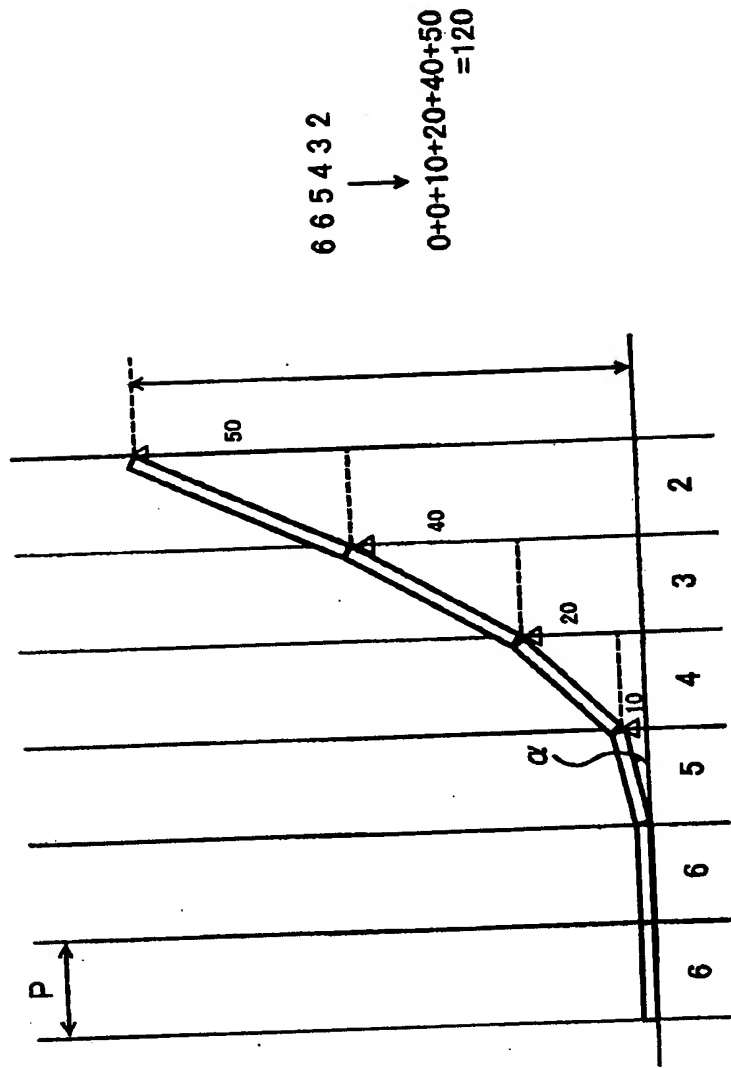


FIG. 9

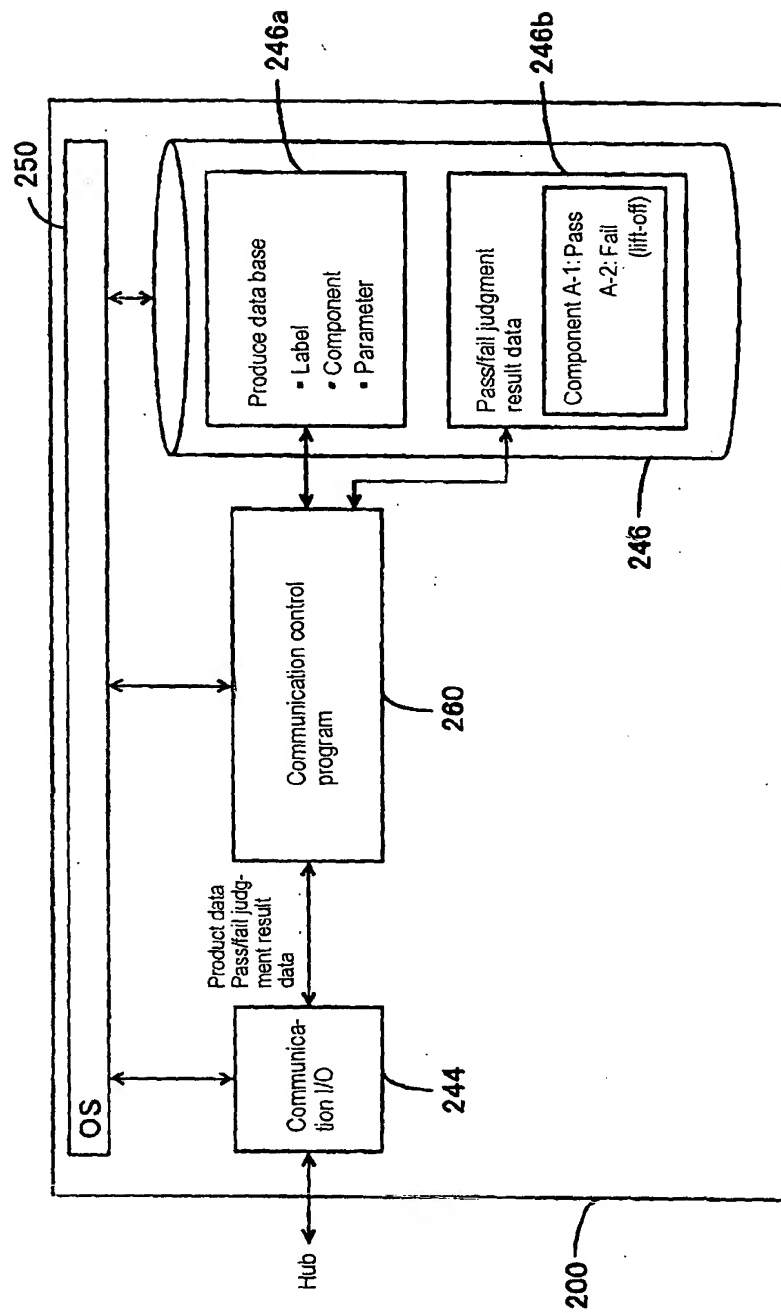


FIG. 10

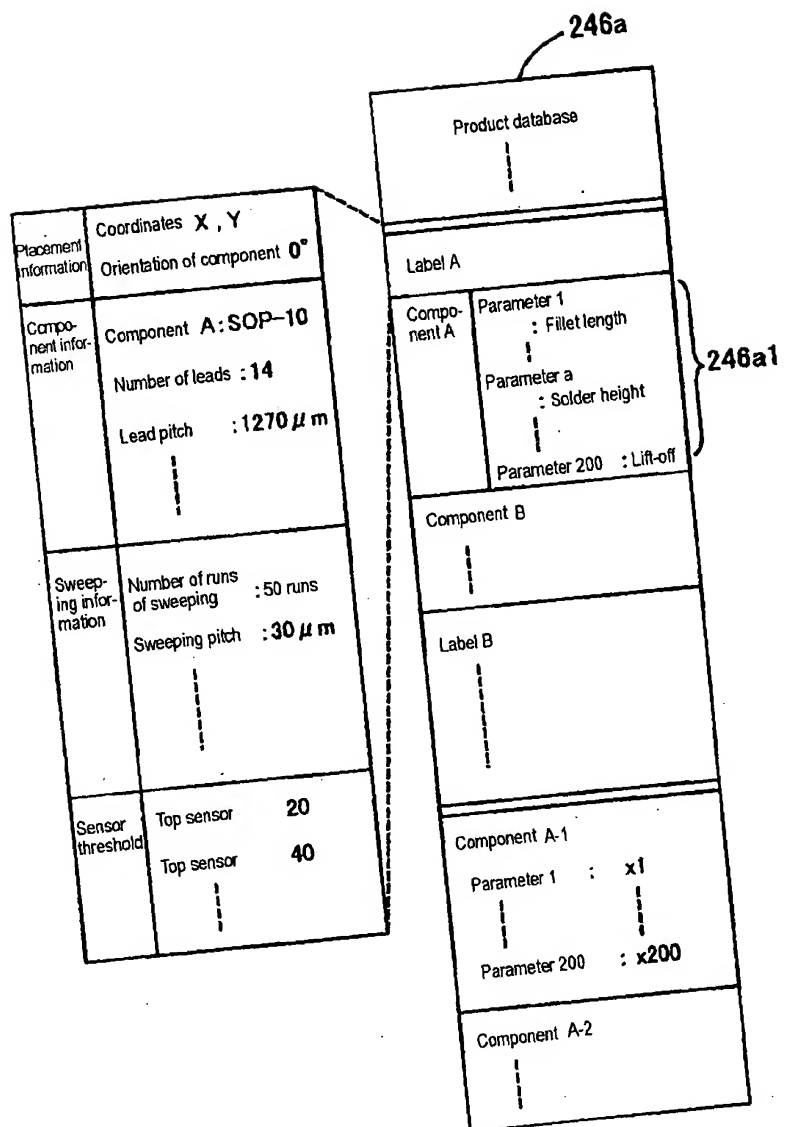


FIG. 11

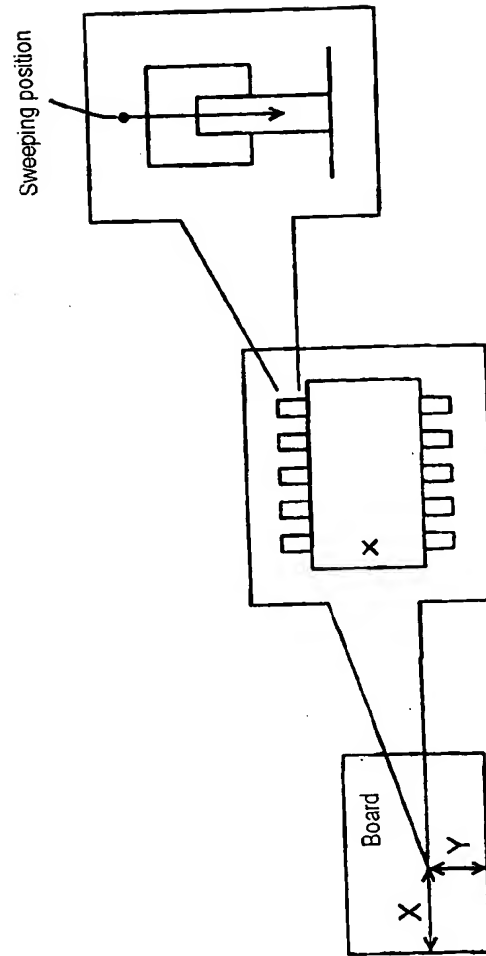


FIG. 12

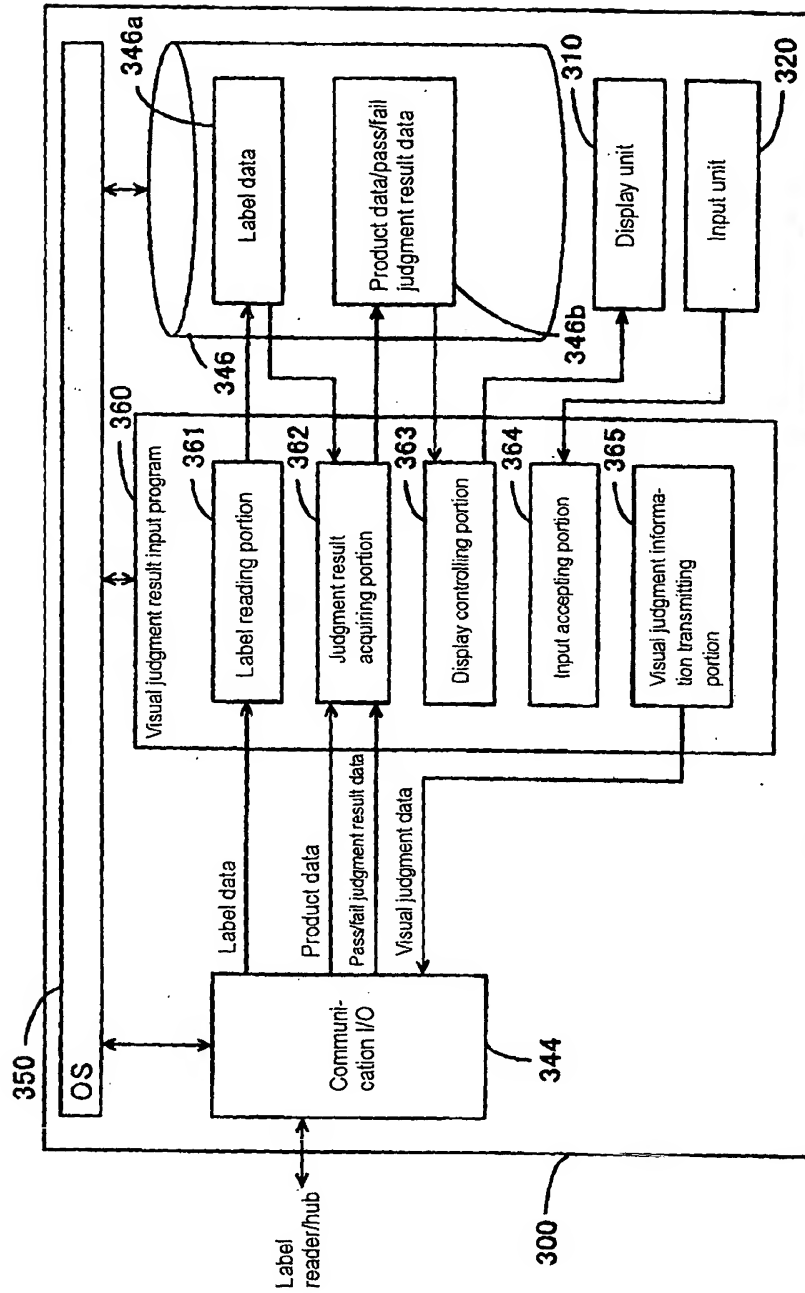
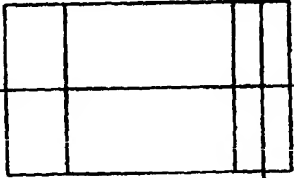


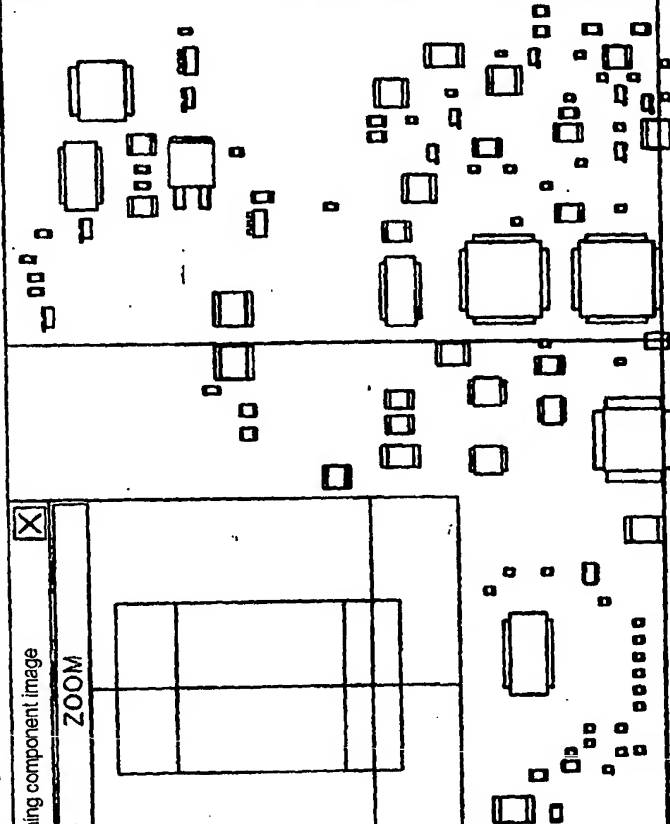
FIG. 13

Visual Judgment Result Input

Zooming component image

ZOOM





Confirmed

NG information

Disinformation

Pin No

Total

1

5

NG type		All NC		All disinformation		Disinformation	
No	NG type	No	NG type	No	NG type	No	NG type
0	Disinformation	0	Disinformation	0	Disinformation	0	Disinformation
1	BRQ	1	BRQ	1	BRQ	1	BRQ
2	Unwetting	2	Unwetting	2	Unwetting	2	Unwetting
3	Lead displacement	3	Lead displacement	3	Lead displacement	3	Lead displacement
4	Lead lift-off	4	Lead lift-off	4	Lead lift-off	4	Lead lift-off
5	Component missing	5	Component missing	5	Component missing	5	Component missing
6	Pad/ply error	6	Pad/ply error	6	Pad/ply error	6	Pad/ply error
7	Lead missing	7	Lead missing	7	Lead missing	7	Lead missing
8	Component displacement	8	Component displacement	8	Component displacement	8	Component displacement
9	Blow hole	9	Blow hole	9	Blow hole	9	Blow hole
10	Too small fillet	10	Too small fillet	10	Too small fillet	10	Too small fillet
11	Excessive solder	11	Excessive solder	11	Excessive solder	11	Excessive solder
12	No acceptance	12	No acceptance	12	No acceptance	12	No acceptance
13	Analysis error	13	Analysis error	13	Analysis error	13	Analysis error
14	Spare	14	Spare	14	Spare	14	Spare
15	Spare	15	Spare	15	Spare	15	Spare
16	Spare	16	Spare	16	Spare	16	Spare
17	Spare	17	Spare	17	Spare	17	Spare
18	Spare	18	Spare	18	Spare	18	Spare
19	NO COUNT	19	NO COUNT	19	NO COUNT	19	NO COUNT
20	Trend toward defective	20	Trend toward defective	20	Trend toward defective	20	Trend toward defective
21	Discard	21	Discard	21	Discard	21	Discard
PIN TOTAL		PIN TOTAL		PIN TOTAL		PIN TOTAL	
2		2		2		2	

Judgment

Confirmed NG

OK

Component type

38

CHIP

Comment

P2B3

Print No

38

Un-Inspected

Confirmed

Disinformation

FIG. 14

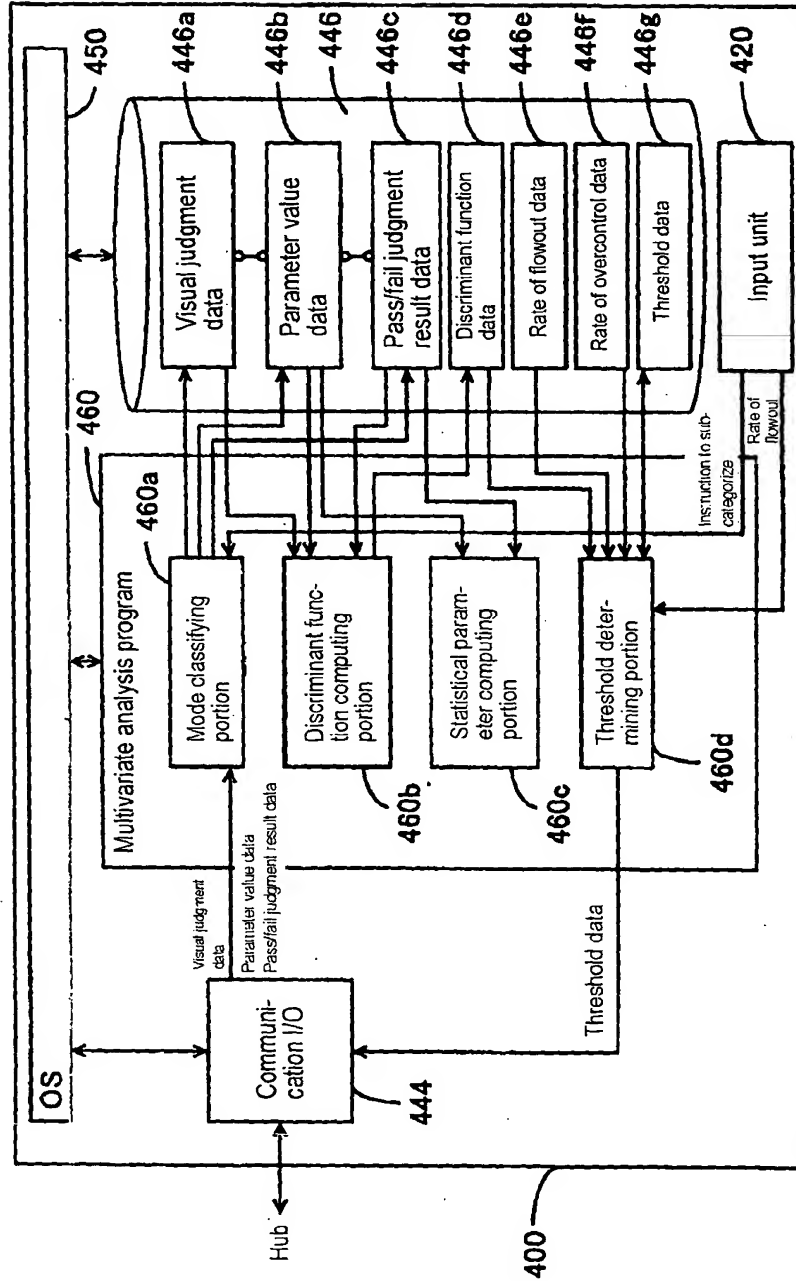


FIG. 15

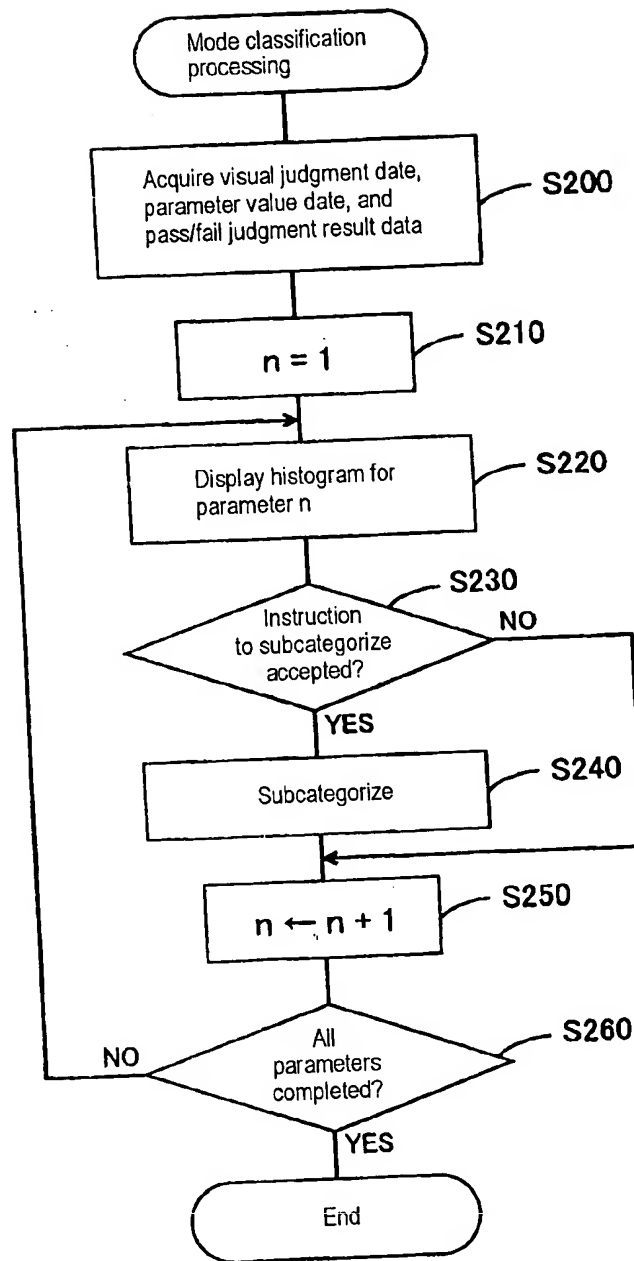


FIG. 16

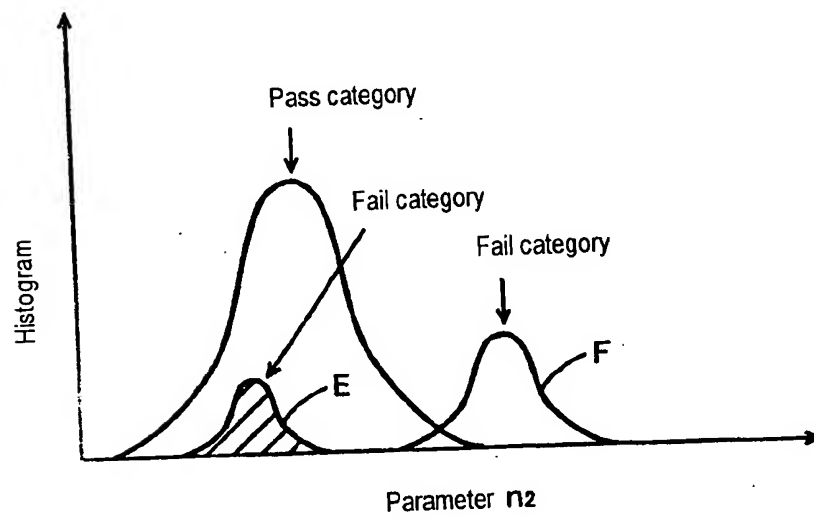
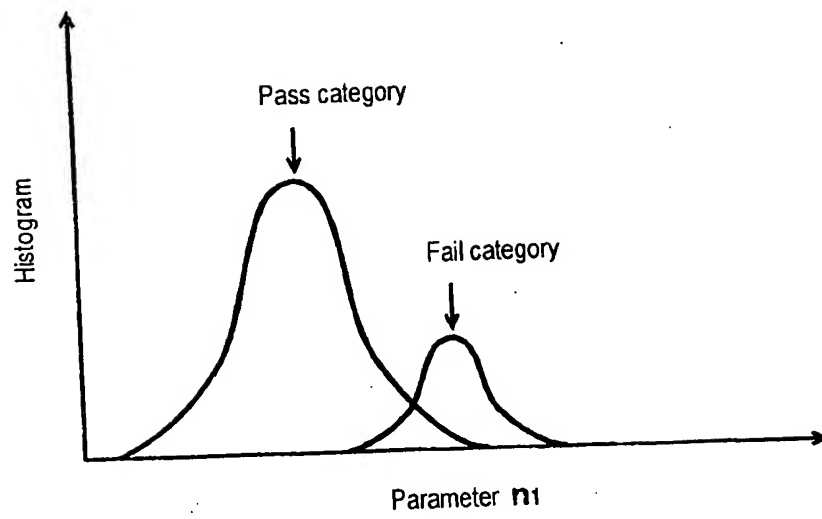


FIG. 17

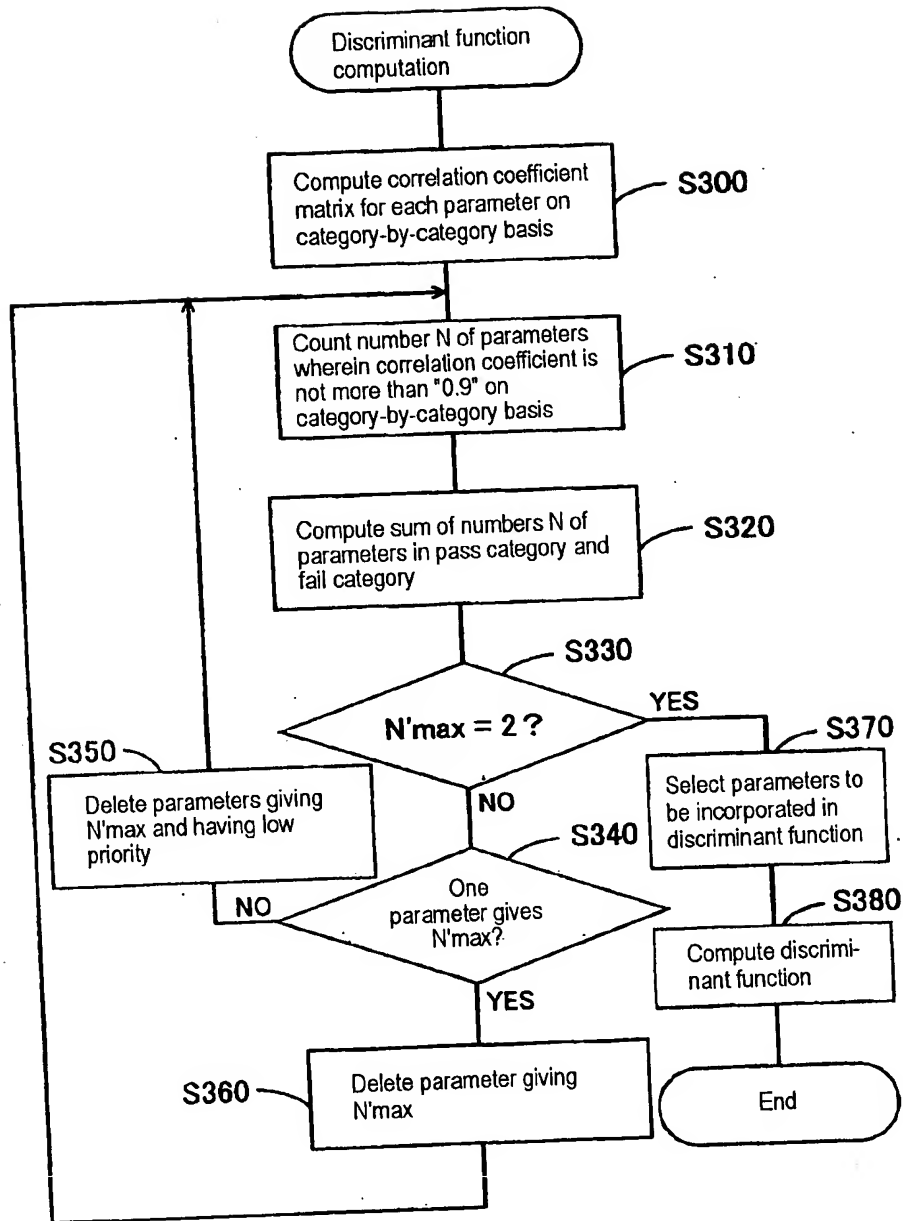


FIG. 18

$$\text{Pass } r_{ij} = \begin{pmatrix} 1 & \text{-----} & \\ & 1 & \text{-----} & r_{ij} \\ & & 1 & \\ & & & 1 \end{pmatrix}$$

Pass parameter	1	2	3	4	5	6	7	8
1	1	r ₁₂	○			○		
2		1		○	○			
3	○		1			○	○	
4		○		1				
5		○			1			
6	○		○			1	○	○
7			○			○	1	
8						○		1
Pass total N	3	3	4	2	2	5	3	2

Fail parameter	1	2	3	4	5	6	7	8
1	1					○	○	
2		1	○					
3		○	1					
4				1		○	○	○
5					1			
6	○			○		1		
7	○			○			1	
8				○				1
Fail total N	3	2	2	4	1	3	3	2

Pass + Fail total N' 6 5 6 6 3 8 6 4

FIG. 19

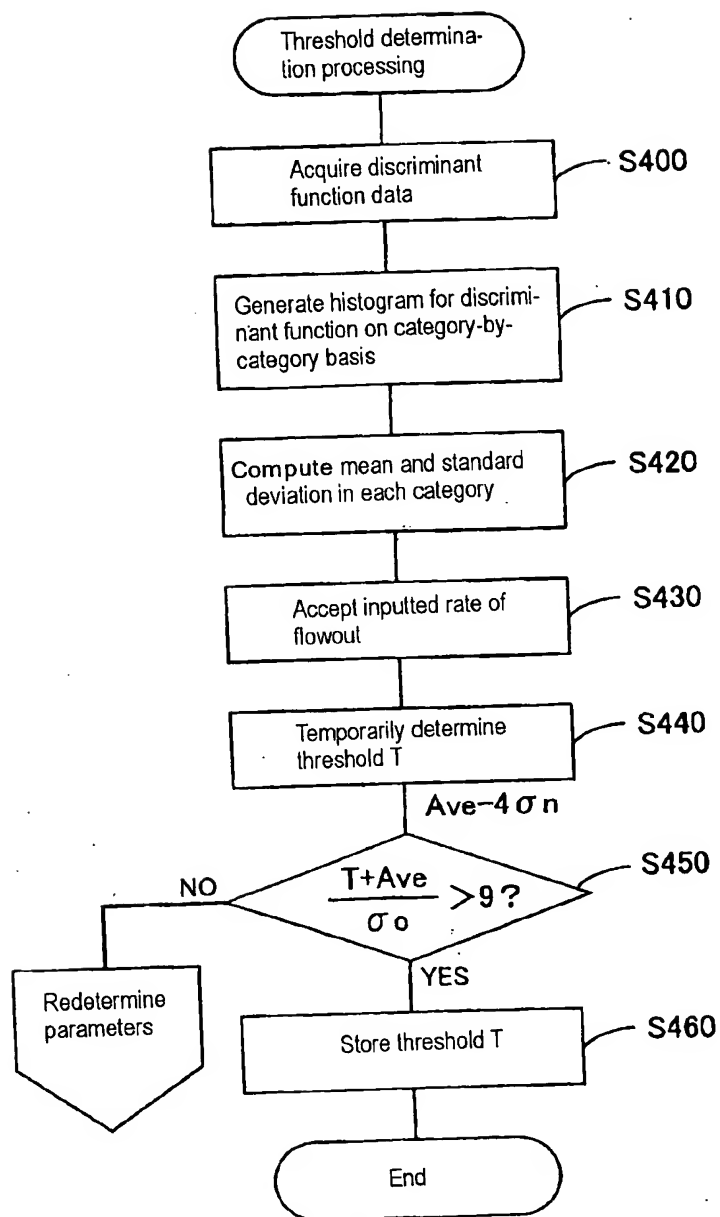


FIG. 20

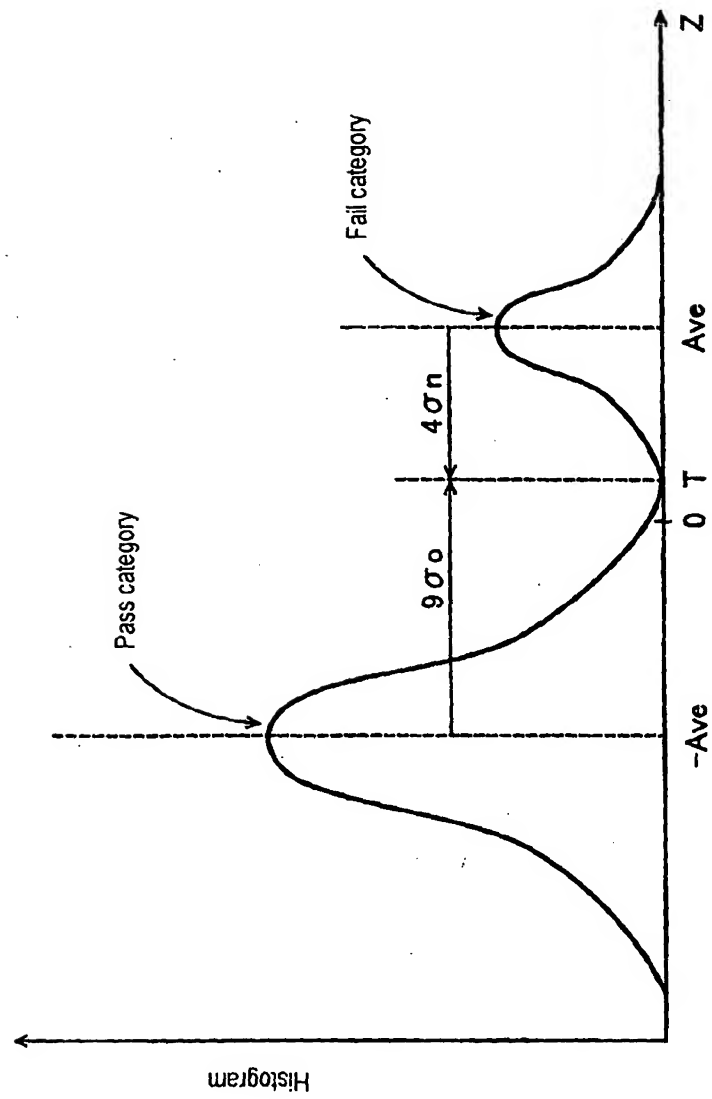


FIG. 21

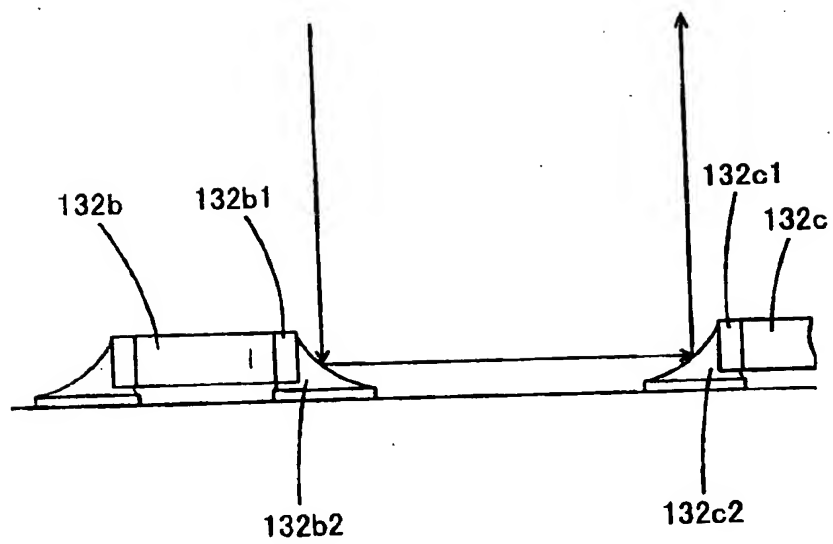


FIG. 22

